

MuTrig Calibration for Run15

RIKEN/RBRC

Itaru Nakagawa

Run List

North (elog entry=1169)					South (elog entry=1168)				
Run#	Threshold	Logic	Pulse Height	Pulse	Run#	Threshold	Logic	Pulse Height	Pulse
417580	10mV	OR	0x00	(2 0 0 0)	417570	10mV	OR	0x00	(1 0 0 0)
417583	20mV	OR	0x17	(2 0 0 0)	417571	20mV	OR	0x17	(1 0 0 0)
417584	40mV	Gap1 Only	0x40	(2 0 0 1)	417577	40mV	Gap1 Only	0x40	(1 0 0 1)
417585	40mV	Gap2 Only	0x40	(2 0 0 2)	417578	40mV	Gap2 Only	0x40	(1 0 0 2)
417586	40mV	Gap3 Only	0x40	(2 1 2 0 3)	417575	40mV	Gap3 Only	0x40	(1 1 2 0 3)

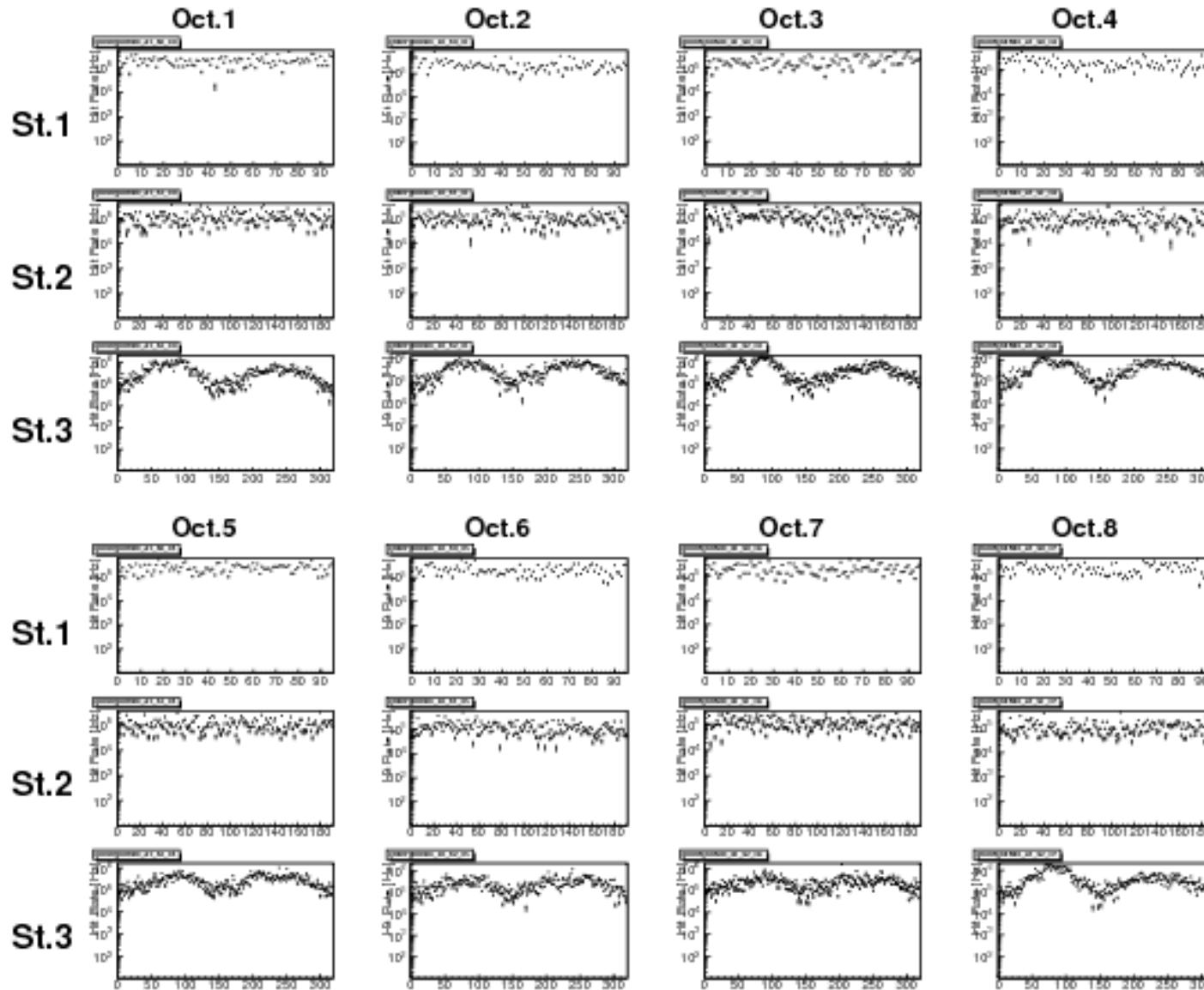
North: Latency=31, delay=3, LL1 width=1, trigdelay=12, LED, process delay=2(St1),1(St2),0(St3)

South: Latency=31, delay=2, LL1 width=1, trigdelay=10, LED, process delay=2(St1),1(St2),0(St3)

NORTH ARM

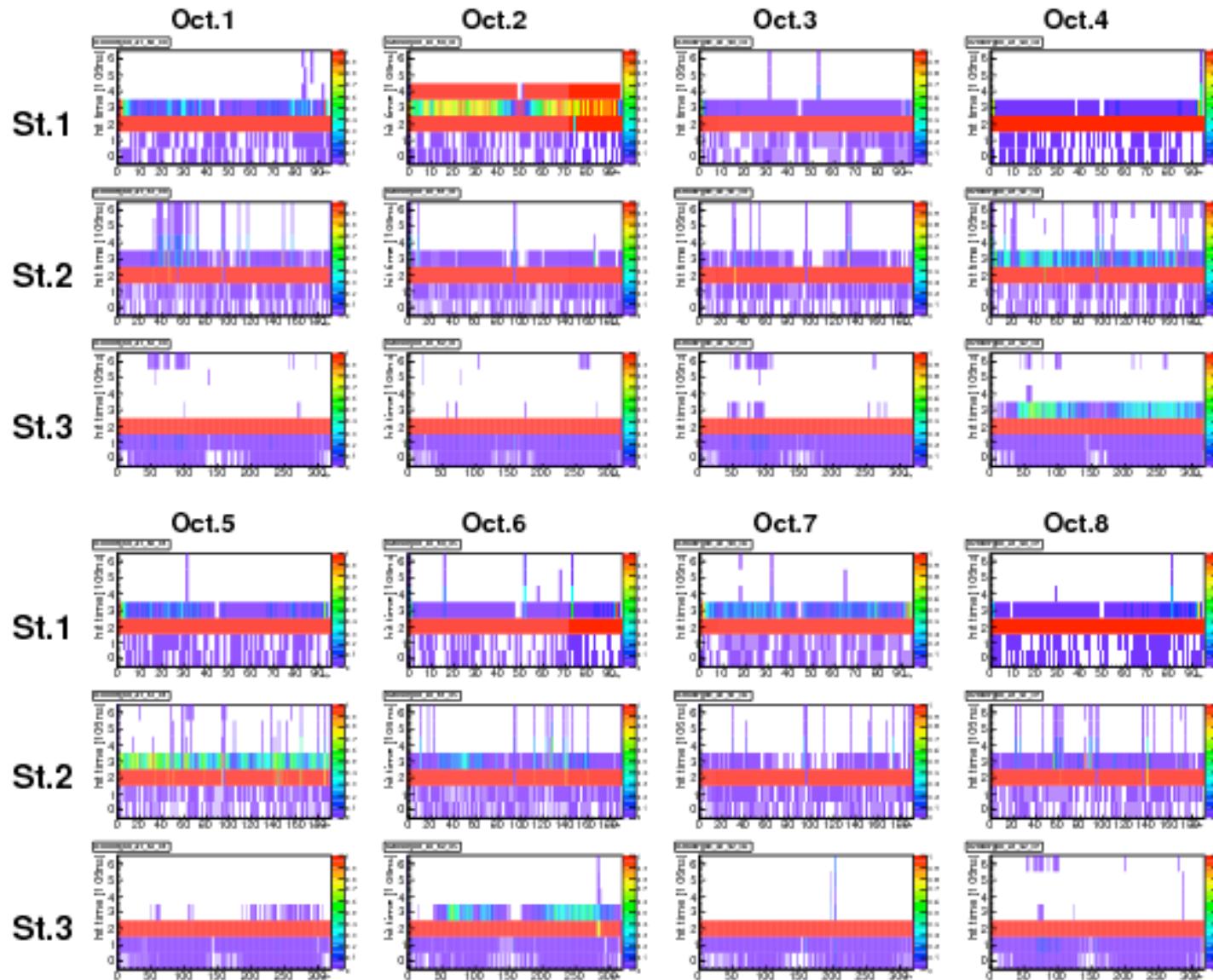
North Noise Level (Run#417580)

LL1 width: 1, Threshold=10mV, Logic:OR



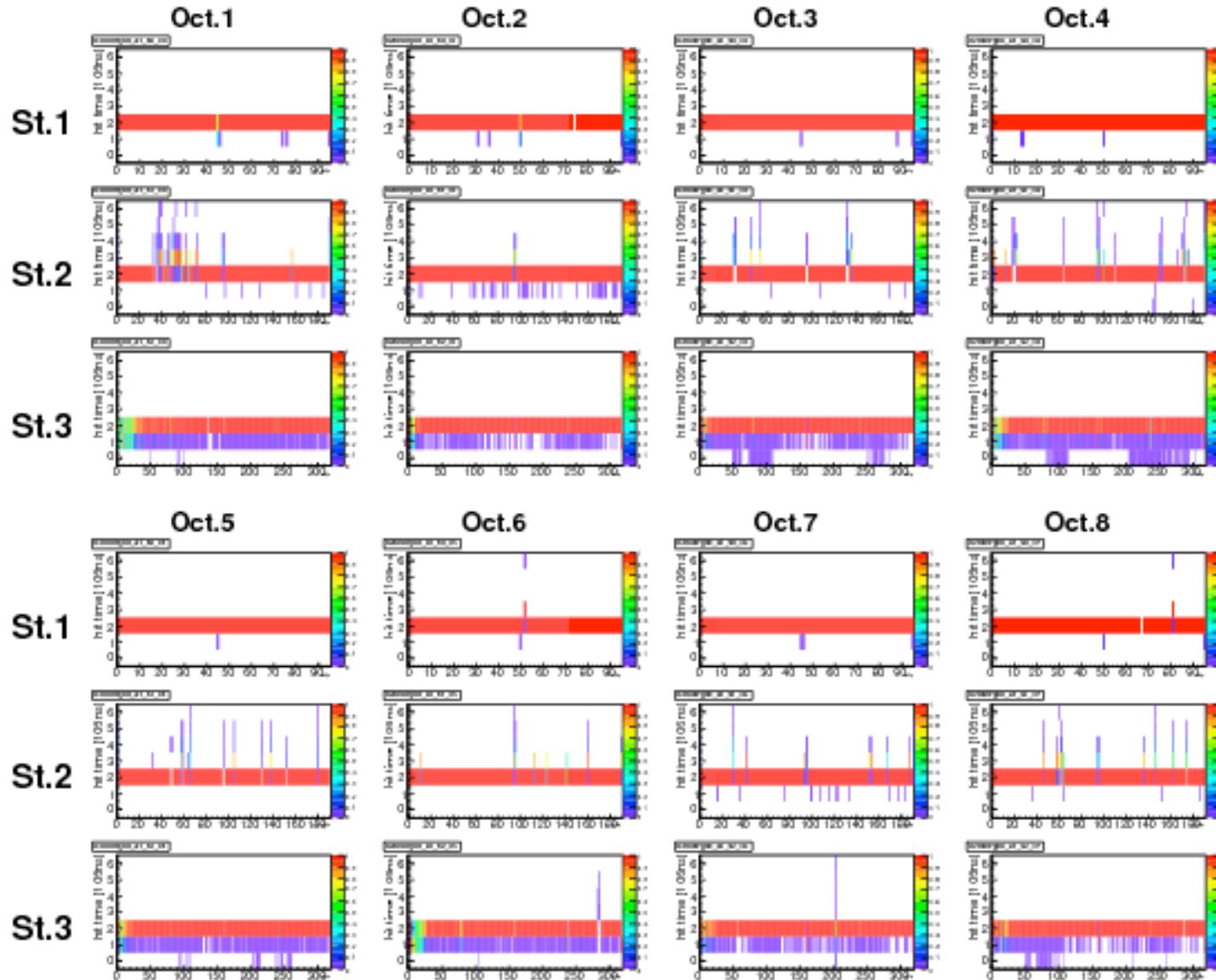
North Hitmap (Run#417583)

LL1 width: 1, Threshold=20mV, Logic:OR, Calib=0x17



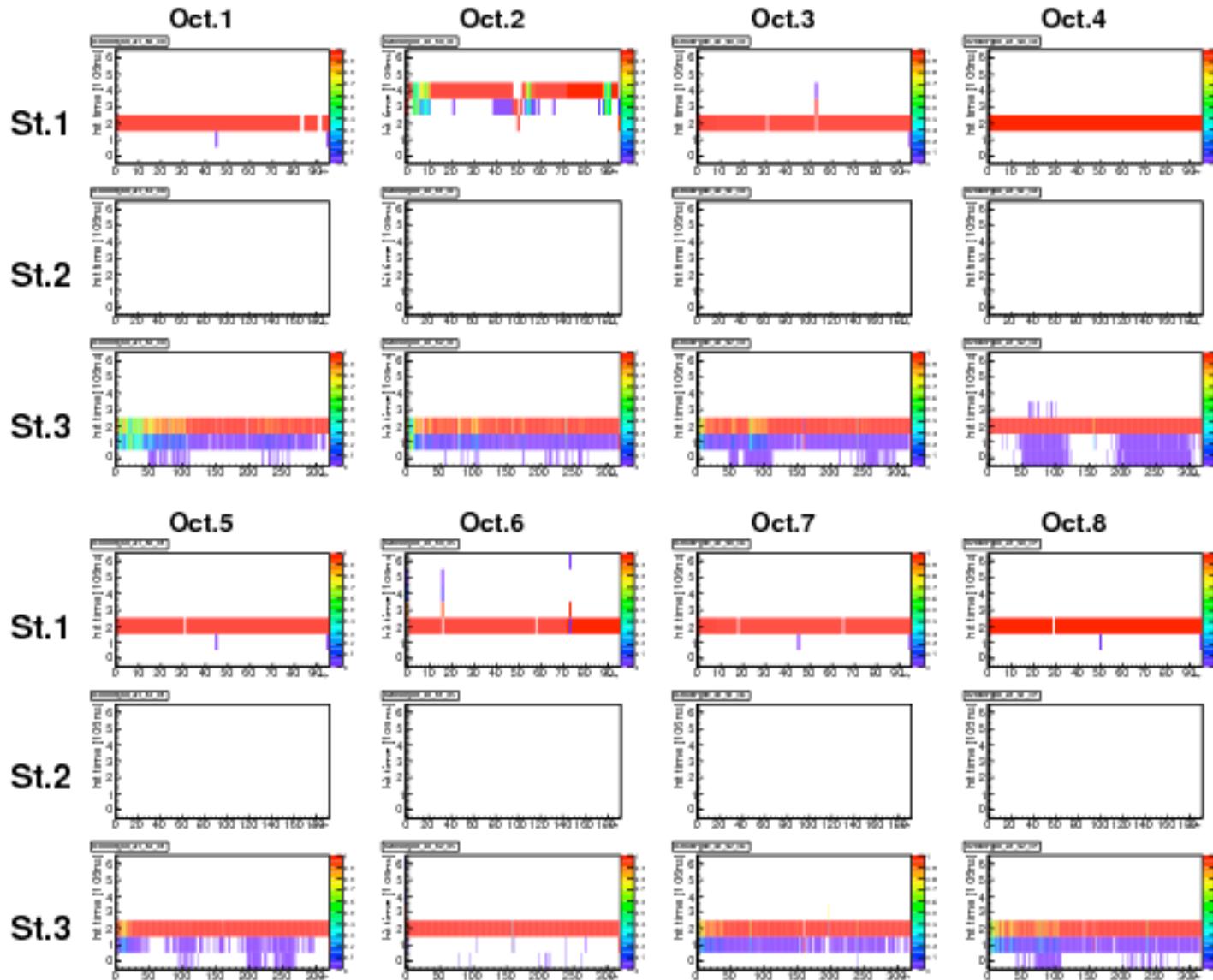
North Gap-1-Only (Run#417584)

LL1 width: 1, Threshold=40mV, Logic:Gap1Only, Calib=0x40



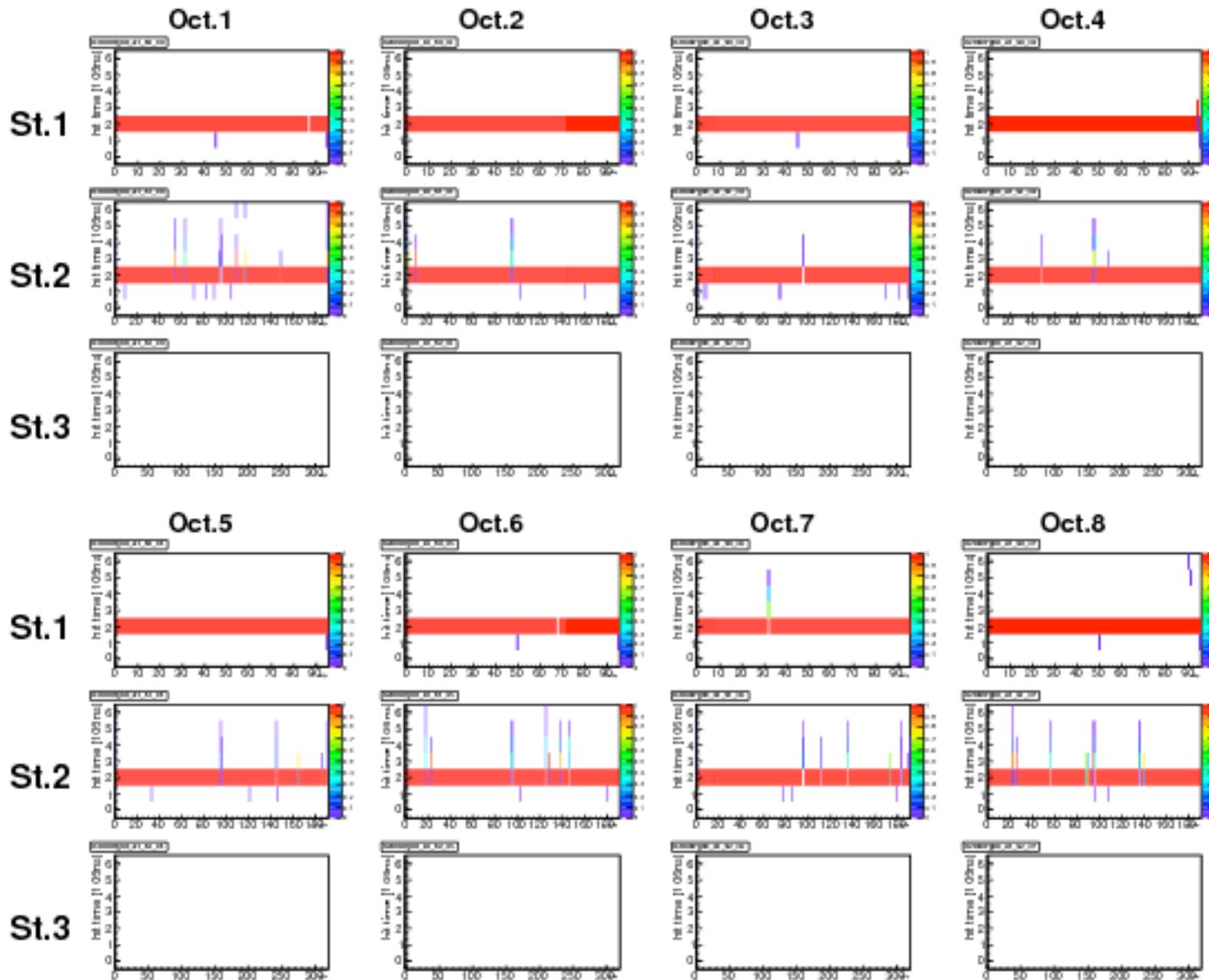
North Gap-2-Only (Run#417585)

LL1 width: 1, Threshold=40mV, Logic:Gap2Only, Calib=0x40



North Gap-3-Only (Run#417586)

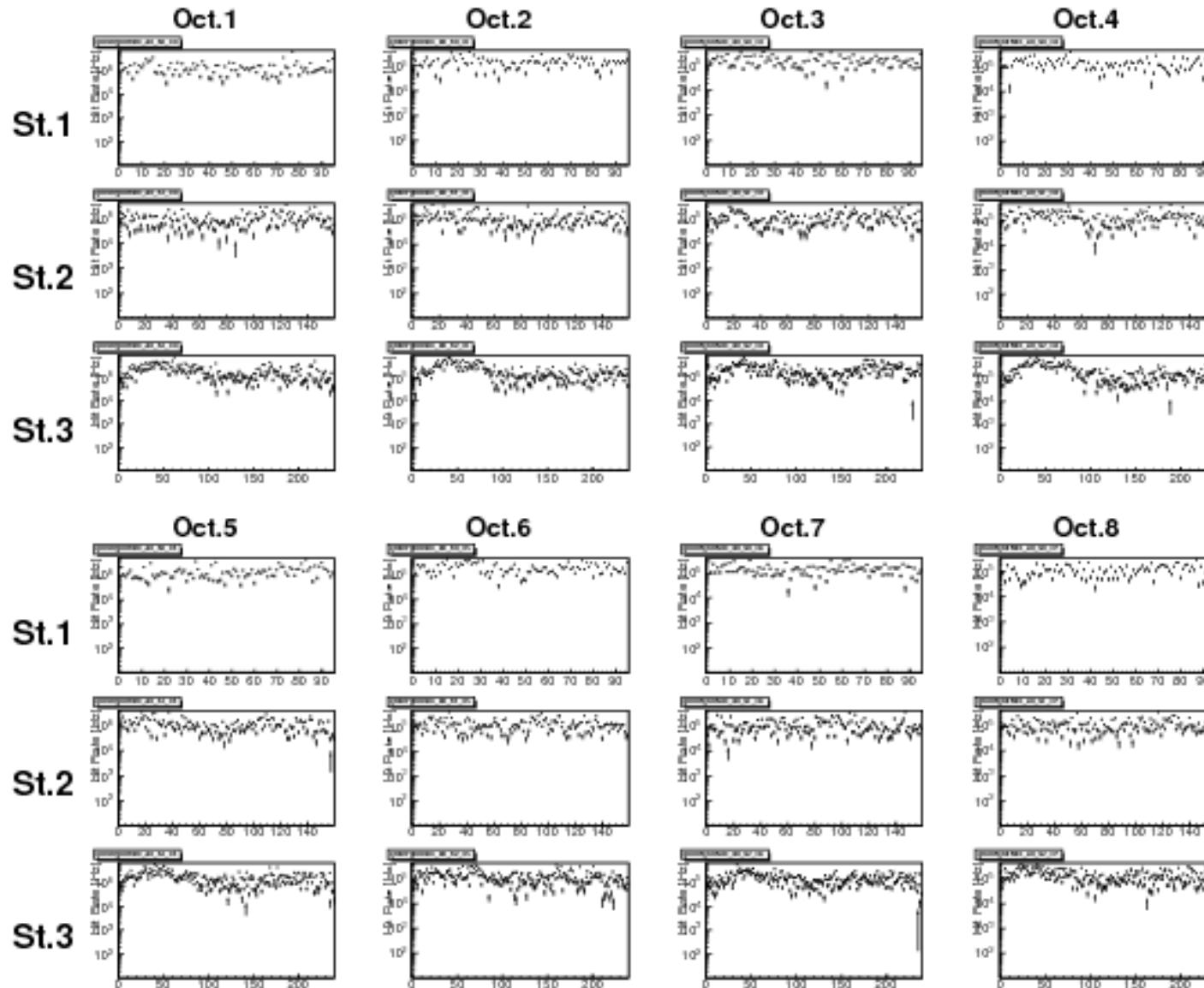
LL1 width: 1, Threshold=40mV, Logic:Gap3Only, Calib=0x40



SOUTH ARM

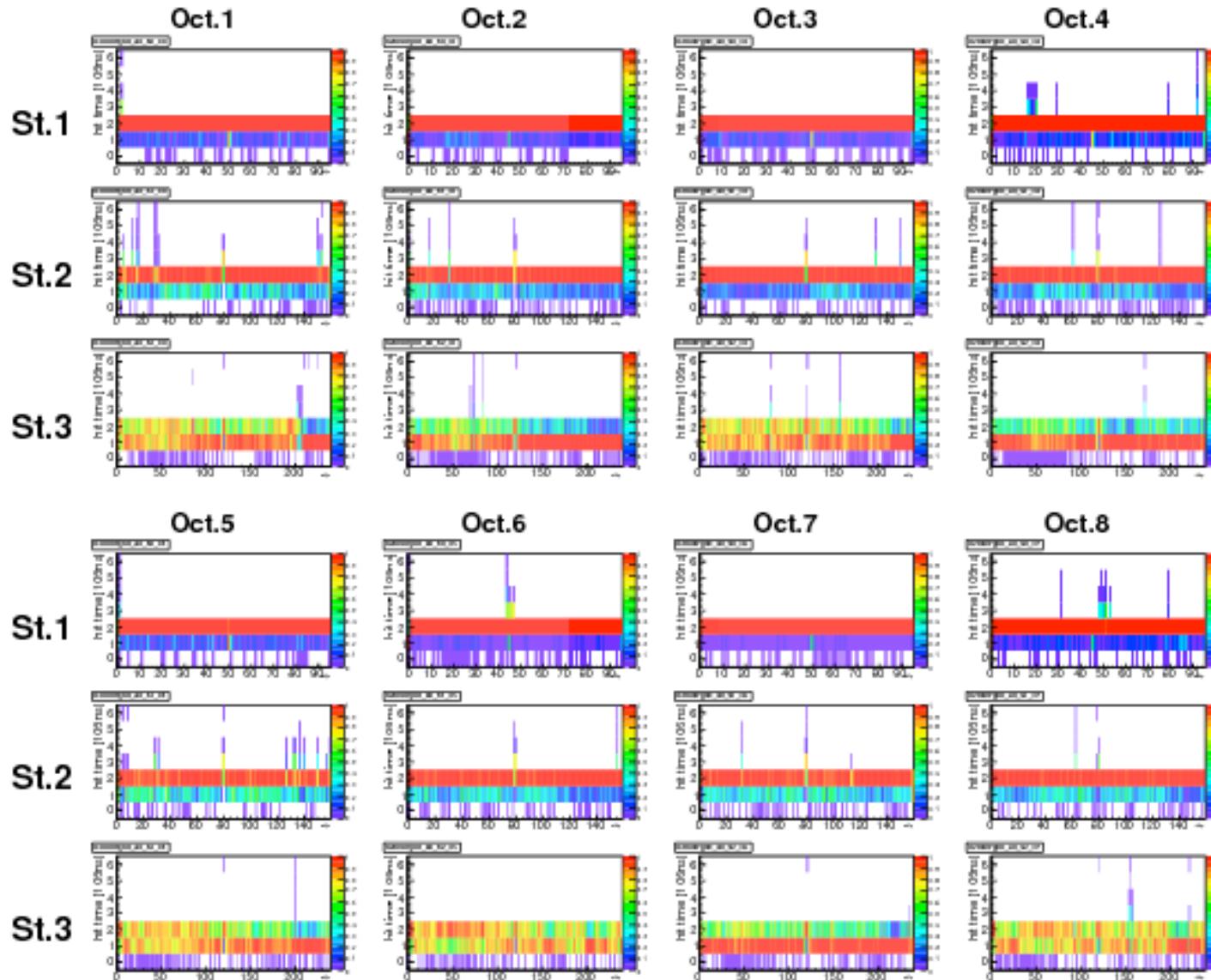
South Noise Level (Run#417570)

LL1 width: 1, Threshold=10mV, Logic:OR



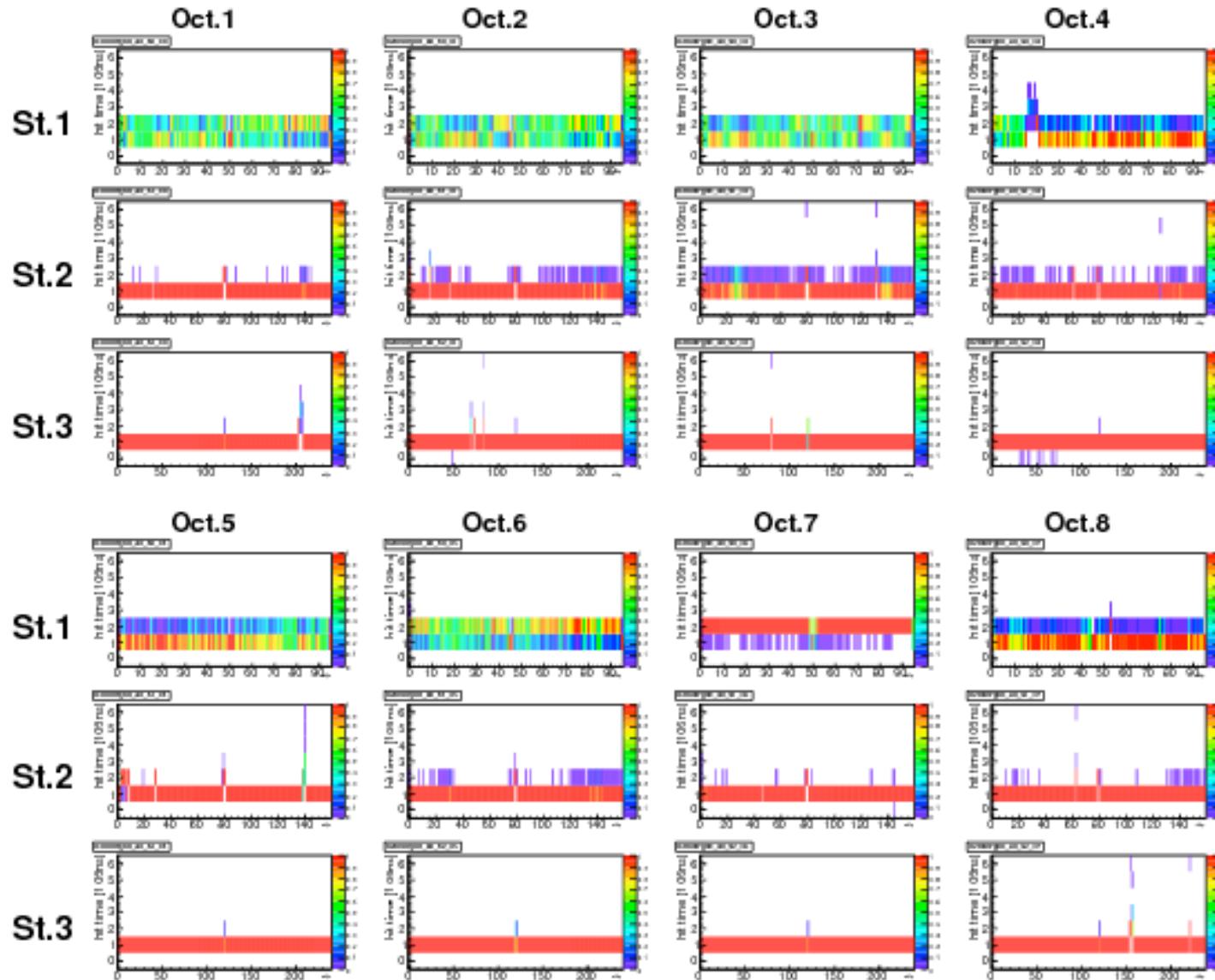
South Hitmap (Run#417571)

LL1 width: 1, Threshold=20mV, Logic:OR, Calib=0x17



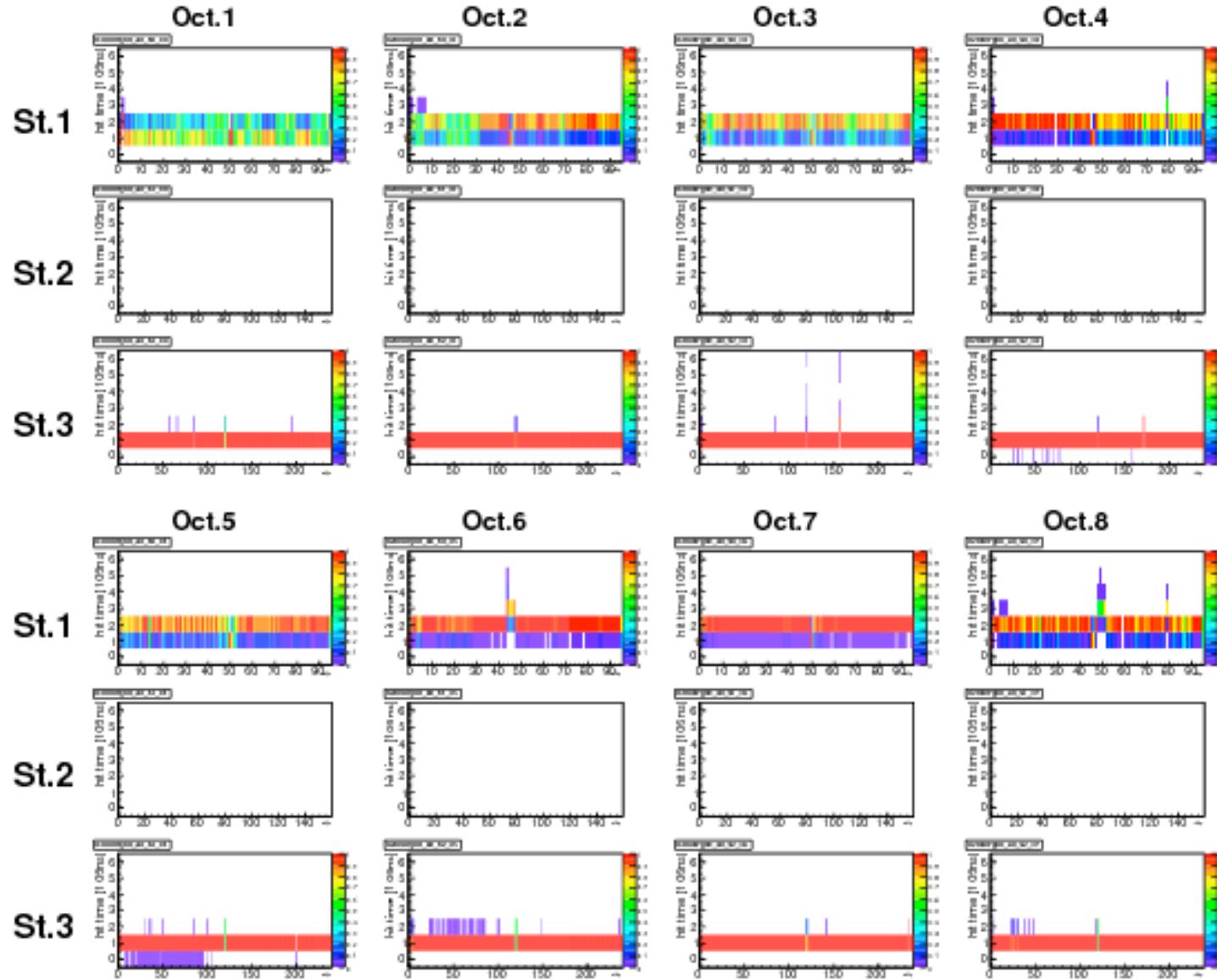
South Gap-1-Only (Run#417577)

LL1 width: 1, Threshold=40mV, Logic:Gap1Only, Calib=0x40



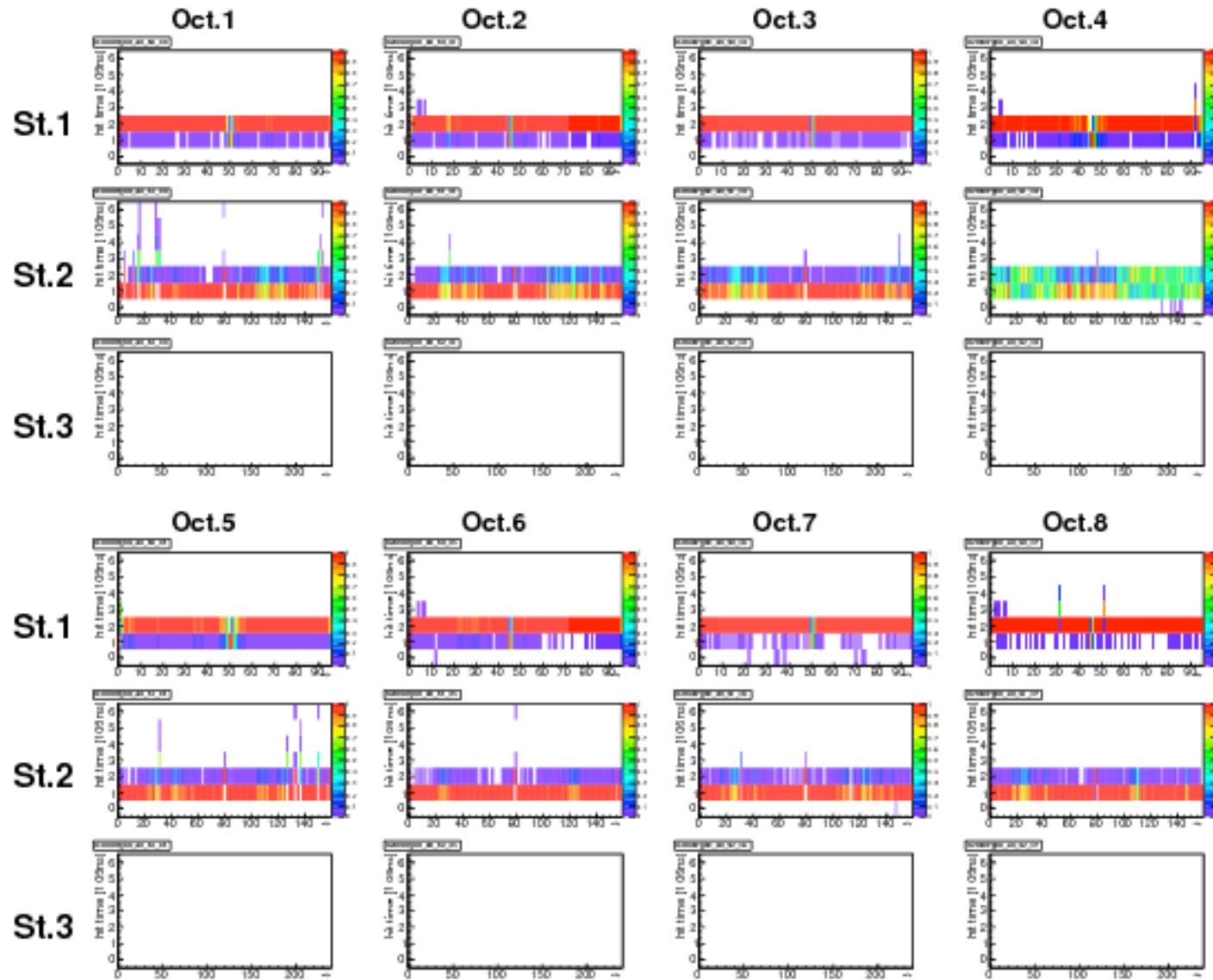
South Gap-2-Only (Run#417578)

LL1 width: 1, Threshold=40mV, Logic:Gap2Only, Calib=0x40



South Gap-3-Only (Run#417575)

LL1 width: 1, Threshold=40mV, Logic:Gap3Only, Calib=0x40



Summary

- (HitMap, Noise) Consistent with Run13 CheckOut Records
- Latency was set to 31 whereas Run13 checkout runs were taken at 32. So hits appear one beam clock off. (not an issue)
- No missing cables. All alive.